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APPLICATION NO. FILING DATE			FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/774,890 02/09/2004			Eugene A. Fitzgerald		ASC-049CI 8754		
TITLE OF INVENTION	: RELAXED SIGE PLA	TFORM FOR HIGH SPI	BED CMOS ELECTRONI	CS AND HIGH SPI	EED ANALOG CIRCUITS		
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE	FEE TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	NO	\$1440	\$0	\$1370	\$1440	09/02/2008	
EXAM	INER	ART UNIT	CLASS-SUBCLASS]			
LE, DUN	IG ANH	2818	438-763000				
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	/Matthew T		Come.	Date_08/2	25/2008		
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